

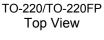
GENERAL DESCRIPTION

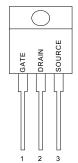
This advanced high voltage MOSFET is designed to withstand high energy in the avalanche mode and switch efficiently. This new high energy device also offers a drain-to-source diode with fast recovery time. Designed for high voltage, high speed switching applications such as power supplies, converters, power motor controls and bridge circuits. ◆

FEATURES

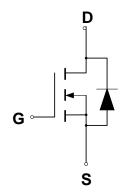
- Higher Current Rating
- ◆ Lower Rds(on)
- ◆ Lower Capacitances
- ◆ Lower Total Gate Charge
 - Tighter VSD Specifications
- Avalanche Energy Specified

PIN CONFIGURATION





SYMBOL



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain to Current — Continuous	I _D	4.0	Α
- Pulsed	I _{DM}	14	
Gate-to-Source Voltage — Continue	V _{GS}	±30	V
 Non-repetitive 	V_{GSM}	±40	V
Total Power Dissipation	P _D		W
TO-220		83	
TO-220FP		30	
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to 150	$^{\circ}\!\mathbb{C}$
Single Pulse Drain-to-Source Avalanche Energy $-$ T $_{ m J}$ = 25 $^{\circ}{ m C}$	E _{AS}	80	mJ
$(V_{DD} = 100V, V_{GS} = 10V, I_L = 4A, L = 10mH, R_G = 25\Omega)$			
Thermal Resistance — Junction to Case	θ_{JC}	1.30	°C/W
 Junction to Ambient 	θ_{JA}	100	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	T _L	260	$^{\circ}\!\mathbb{C}$



ORDERING INFORMATION

Part Number	Package
CMT04N60GN220*	TO-220
CMT04N60XN220*	TO-220
CMT04N60GN220FP*	TO-220 Full Package
CMT04N60XN220FP*	TO-220 Full Package

*Note: G : Suffix for Pb Free Product

X : Suffix for Halogen and Pb Free Product

ELECTRICAL CHARACTERISTICS

Unless otherwise specified, $T_J = 25^{\circ}C$.

			CMT04N60			
Characteristic			Min	Тур	Max	Units
Drain-Source Breakdown Voltage	V _{(BR)DSS}	600			V	
$(V_{GS} = 0 \text{ V}, I_D = 250 \ \mu \text{ A})$						
Drain-Source Leakage Current		I _{DSS}				uA
$(V_{DS} = 600 \text{ V}, V_{GS} = 0 \text{ V})$					1	
Gate-Source Leakage Current-F	orward	I _{GSSF}			100	nA
$(V_{gsf} = 30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate-Source Leakage Current-R	everse	I_{GSSR}			100	nA
$(V_{gsr} = -30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate Threshold Voltage		$V_{GS(th)}$	2.0		4.0	V
$(V_{DS} = V_{GS}, I_D = 250 \ \mu A)$						
Static Drain-Source On-Resistan	ce (V _{GS} = 10 V, I _D = 2.0A) *	R _{DS(on)}			2.2	Ω
Forward Transconductance (V _{DS}	= 50 V, I _D = 2.0 A) *	g FS	2.5			mhos
Input Capacitance	$(V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$	C _{iss}		540	760	pF
Output Capacitance	f = 1.0 MHz)	Coss		125	180	pF
Reverse Transfer Capacitance	1 - 1.0 Wil 12)	C _{rss}		8.0	20	pF
Turn-On Delay Time	$(V_{DD} = 300 \text{ V}, I_D = 4.0 \text{ A},$	t _{d(on)}		12	20	ns
Rise Time	$V_{GS} = 10 \text{ V},$	t _r		7.0	10	ns
Turn-Off Delay Time	$R_{\rm G} = 9.1\Omega$) *	$t_{d(off)}$		19	40	ns
Fall Time	11.6 – 3.122)	t _f		10	20	ns
Total Gate Charge	$(V_{DS} = 480 \text{ V}, I_{D} = 4.0 \text{ A},$	Q_g		5.0	10	nC
Gate-Source Charge	$V_{GS} = 400 \text{ V}, I_D = 4.0 \text{ A},$ $V_{GS} = 10 \text{ V})^*$	Q_{gs}		2.7		nC
Gate-Drain Charge	VGS - 10 V)	Q_{gd}		2.0		nC
Internal Drain Inductance	L_{D}		4.5		nΗ	
(Measured from the drain lead 0.25" from package to center of die)						
Internal Drain Inductance	Ls		7.5		nΗ	
(Measured from the source lea						
pad)						
SOURCE-DRAIN DIODE CHAR	ACTERISTICS		Γ			
Forward On-Voltage(1)	$(I_S = 4.0 \text{ A},$	V _{SD}			1.5	V
Forward Turn-On Time	$d_{IS}/d_{t} = 100A/\mu s$	t _{on}		**		ns
Reverse Recovery Time	,	t _{rr}		655		ns

^{*} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%

^{**} Negligible, Dominated by circuit inductance



TYPICAL CHARACTERISTICS

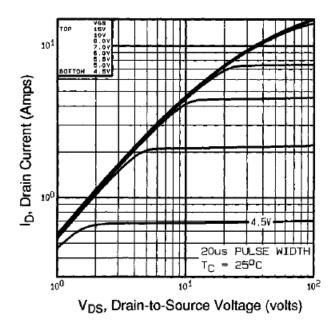


Fig 1. Typical Output Characteristics, Tc=25°C

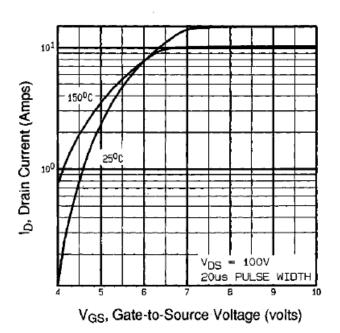


Fig 3. Typical Transfer Characteristics

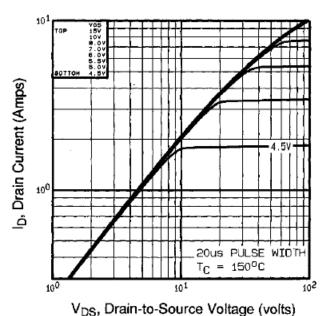


Fig 2. Typical Output Characteristics,

Tc=150°C

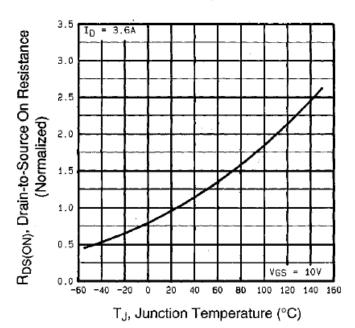


Fig 4. Normalized On-Resistance Vs. Temperature



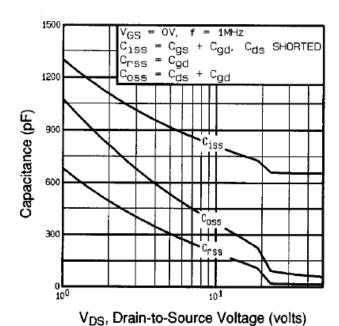


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

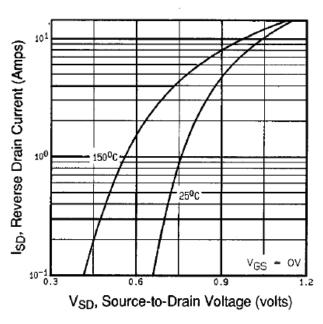


Fig 7. Typical Source-Drain Diode Forward Voltage

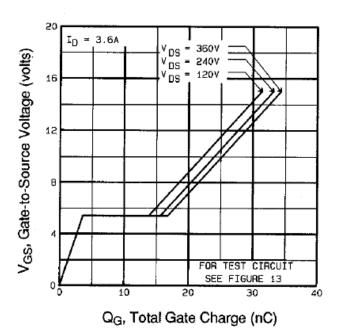


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

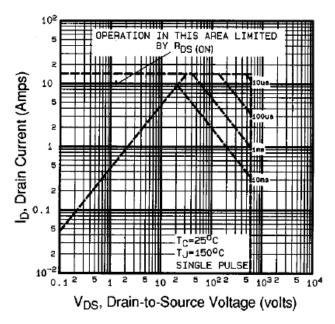
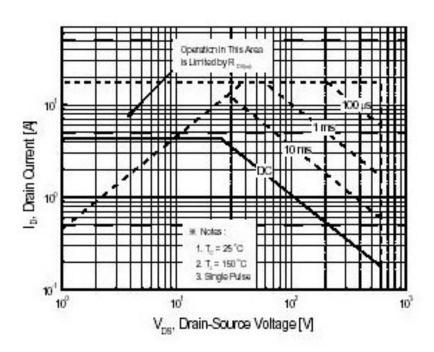


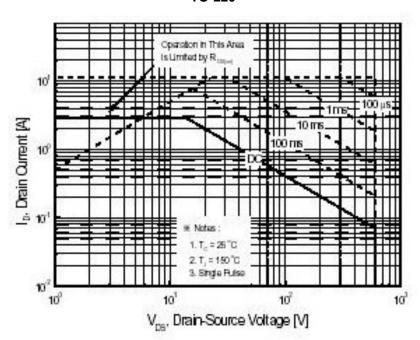
Fig 8. Maximum Safe Operating Area





Maximum Safe Operating Area

TO-220

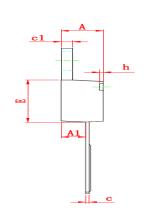


Maximum Safe Operating Area



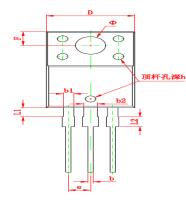
PACKAGE DIMENSION

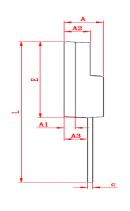
TO-220



O	Dimensions	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min	Max	Min	Max		
Α	4.470	4.670	0.176	0.184		
A1	2.520	2.820	0.099	0.111		
ь	0.710	0.910	0.028	0.036		
b1	1.170	1.370	0.046	0.054		
С	0.310	0.530	0.012	0.021		
c1	1.170	1.370	0.046	0.054		
D	10.010	10.310	0.394	0.406		
E	8.500	8.900	0.335	0.350		
E1	12.060	12.460	0.475	0.491		
е	2.540 TYP		0.100	TYP		
e1	4.980	5.180	0.196	0.204		
F	2.590	2.890	0.102	0.114		
h	0.000	0.300	0.000	0.012		
L	13.400	13.800	0.528	0.543		
L1	3.560	3.960	0.140	0.156		
Φ	3.735	3.935	0.147	0.155		

TO-220FP





C	Symbol Dimensions In Millimeters		Dimension	s In Inches
Symbol	Min	Max	Min	Max
A	4.300	4.700	0.169	0.185
A1	1.300 REF		0.051 REF	
A2	2.800	3.200	0.110	0.126
A3	2.500	2.900	0.098	0.114
b	0.500	0.750	0.020	0.030
b1	1.100	1.350	0.043	0.053
b2	1.500	1.750	0.059	0.069
С	0.500	0.750	0.020	0.030
D	9.960	10.360	0.392	0.408
E	14.800	15.200	0.583	0.598
е	2.540 TYP		0.100 TYP	
F	2.700 REF		0.106 REF	
Φ	3.500 REF		0.138	REF
h	0.000	0.300	0.000	0.012
L	28.000	28.400	1.102	1.118
L1	1.700	1.900	0.067	0.075
L2	1.900	2.100	0.075	0.083



IMPORTANT NOTICE

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